

| L Number | Hits | Search Text | DB | Time stamp |
|----------|------|---|----------------------|------------------|
| 7 | 12 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 interface) | USPAT | 2003/03/31 10:32 |
| 8 | 0 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 interface) | IBM_TDB | 2003/03/31 10:32 |
| 9 | 0 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 controller) | IBM_TDB | 2003/03/31 10:32 |
| 10 | 3 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 interface) | EPO; JPO | 2003/03/31 10:59 |
| 13 | 67 | "4860290" | USPAT | 2003/03/31 11:03 |
| 14 | 0 | "4860290" and (cross\$3 near1 over) | USPAT | 2003/03/31 11:03 |
| 15 | 0 | "4860290" and (frequency adj1 domain\$1) | USPAT | 2003/03/31 11:04 |
| 16 | 22 | "4860290" and (domain\$1) | USPAT | 2003/03/31 11:07 |
| 17 | 147 | "5329471" | USPAT | 2003/03/31 11:08 |
| - | 24 | "5471587" | USPAT | 2003/03/28 15:41 |
| - | 17 | (multiple or variable) and "5471587" | USPAT | 2003/03/27 14:52 |
| - | 1 | ((multiple or variable) same cross\$5) and "5471587" | USPAT | 2003/03/27 17:34 |
| - | 3 | ((multiple or variable) and cross\$5) and "5471587" | USPAT | 2003/03/27 17:50 |
| - | 6 | "5915107" | USPAT | 2003/03/27 17:50 |
| - | 224 | ((cross\$3 near1 over) or (cross-over\$1) or crossover\$1) near8 clock | USPAT | 2003/03/28 15:43 |
| - | 41 | ((cross\$3 near1 over) or (cross-over\$1) or crossover\$1) near8 clock | DERWENT; IBM_TDB | 2003/03/28 15:44 |
| - | 0 | (((cross\$3 near1 over) or (cross-over\$1) or crossover\$1) near8 clock) with domain\$1 | DERWENT; IBM_TDB | 2003/03/28 15:44 |
| - | 11 | (((cross\$3 near1 over) or (cross-over\$1) or crossover\$1) near8 clock) with domain\$1 | USPAT | 2003/03/28 16:04 |
| - | 2 | ((cross\$3 near1 over) or (cross-over\$1) or crossover\$1) near8 clock | EPO | 2003/03/28 15:45 |
| - | 29 | ((cross\$3 near1 over) or (cross-over\$1) or crossover\$1) near8 clock | JPO | 2003/03/28 15:45 |
| - | 22 | (memory near2 domain\$1) with ((processor or cpu) near2 domain\$1) | USPAT | 2003/03/28 17:01 |
| - | 1 | 6349392.URPN. | USPAT | 2003/03/28 16:49 |
| - | 3 | ("4244048" "4441075" "4701920").PN. | USPAT | 2003/03/28 16:49 |
| - | 9 | memory near1 control near2 domain\$1 | USPAT | 2003/03/28 16:53 |
| - | 12 | memory near1 control near2 domain\$1 | EPO; JPO; DERWENT | 2003/03/28 16:53 |
| - | 0 | memory near1 control near2 domain\$1 | IBM_TDB | 2003/03/28 16:56 |
| - | 9 | memory near1 control near2 domain\$1 | USPAT | 2003/03/28 16:57 |
| - | 9 | ("4000460" "4070565" "4216539" "4244048" "4441075" "4701920" "4744084" "6085336" "6349392").PN. | USPAT | 2003/03/28 17:00 |
| - | 2743 | (((processor or cpu) near2 (integrated near1 circuit\$2))) | USPAT | 2003/03/28 17:03 |
| - | 139 | (((processor or cpu) near2 (integrated near1 circuit\$2))) | JPO | 2003/03/28 17:03 |
| - | 65 | (((processor or cpu) near2 (integrated near1 circuit\$2))) | EPO | 2003/03/28 17:02 |
| - | 2 | (((processor or cpu) near2 (integrated near1 circuit\$2))) | IBM_TDB | 2003/03/28 17:02 |
| - | 1 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 control\$3) | EPO | 2003/03/28 17:04 |
| - | 3 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 control\$3) | JPO | 2003/03/28 17:04 |
| - | 57 | (((processor or cpu) near2 (integrated near1 circuit\$2))) with (memory near2 control\$3) | USPAT | 2003/03/31 10:10 |